

REMARKS/ARGUMENTS

Claims 1-9 and 11-13 are pending. Claims 10 and 14 have been canceled without prejudice or disclaimer.

- <Claims 9 and 10, section 112, first paragraph rejection
- <Claims 9-11, section 112, 2nd paragraph rejection
- <Claims 1-7, section 102 rejection
- <Claims 8 and 14, section 103 rejection
- <Claim 11, section 103 rejection
- <Claims 12 and 13, section 103 rejection

Rejection of Claims 9 and 10 Under Section 112, First Paragraph

Claim 10 has been canceled without prejudice or disclaimer, and so this rejection of claim 10 is moot.

As to claim 9, the examiner indicated that the recited equation only works for i=1. The recited equation reads as follows:

$$C_i = C_{i-1} + \sum_{j=1}^n k \cdot D_j .$$

For i=1, C₁ is computed based on its previous value, C₀ in this case, and on the other term.

For i=2, C₂ is computed based on its previous value, namely C₁, and on the other term. The value of C₁ is computed as discussed above.

For i=3, C₃ is computed based on its previous value, namely C₂, and on the other term. The value of C₂ is computed as discussed above, which in turn relies of C₁ which is computed as discussed above.

This is conventional mathematical notation of a very straightforward equation that relies on previous values. If the foregoing explanation is not sufficient to overcome this rejection of claim 9, the examiner is respectfully requested to be more specific in his explanation as to how this equation only works for i=1. This rejection of claim 9 is believed to be traversed.

Rejection of Claims 9-11 Under Section 112, Second Paragraph

Claim 10 has been canceled without prejudice or disclaimer, and so this rejection of claim 10 is moot.

Claim 9 has been amended to include the label “equation 1.” As amended, this rejection of claim 9 is believed to be overcome.

Claim 11 has been amended to cancel the reference to “column n”. As amended, this rejection of claim 11 is believed to be overcome.

Rejections Under Section 102 and Section 103

Independent claim 1, as amended, is representative and recites distinguishing limitations common among the independent claims. For brevity, the following remarks will be made with reference to the language of amended claim 1. However, these remarks are also applicable to claims 4, 5, 8, and 12 which contain similar limitations. No new matter has been added; see, for example, paragraphs [0026] and [0027] in the specification as originally filed.

Claim 1 recites in part a “signal correction circuit that corrects each drive signal using a cumulative value of the video data corresponding to said drive signals.”

Yamazaki was cited in the Section 102 and 103 rejections of the independent claims. Yamazaki teaches dividing a horizontal interval into periods, and producing correction data in each period. See for example Yamazaki at paragraphs [45]-[57]. Yamazaki discloses correcting drive signals to compensate for voltage drop caused by wiring resistance. However, Yamazaki does not teach or even suggest the recited “signal correction circuit for correcting each of the drive signals to be supplied to the plurality of data lines, wherein the signal correction circuit corrects the drive signals using a cumulative value of the video data corresponding to said drive signals.”

The pending claims are believed to be allowable over the cited art to the extent that Yamazaki was relied on for the Section 102 and primarily relied on for the 103 rejections of the claims.

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PATENT

CONCLUSION

In view of the foregoing, Applicants believe all claims now pending in this Application are in condition for allowance. The issuance of a formal Notice of Allowance at an early date is respectfully requested.

If the Examiner believes a telephone conference would expedite prosecution of this application, please telephone the undersigned at 650-326-2400.

Respectfully submitted,


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